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(54) Title: <b>SYSTEM AND METHOD FOR CONTROLLING THE LEVEL OF SIGNALS OUTPUT TO TRANSMISSION MEDIA IN A DISTRIBUTED ANTENNA NETWORK</b>			
<pre> graph LR     Antenna((150)) -- 129n --&gt; GainController[GAIN CONTROLLER]     GainController -- 142n --&gt; Transceiver[TRANSCEIVER CIRCUITRY]     Transceiver -- 128n --&gt; GainController     Transceiver -- 110n --&gt; SignalStrength[SIGNAL STRENGTH COMPARATOR]     SignalStrength -- 127n --&gt; Transceiver     Transceiver -- 125n --&gt; Antenna     Transceiver -- 102n --&gt; Output(( ))     GainController -- 100n --&gt; Output     SignalStrength -- 130n --&gt; Output   </pre>			
(57) Abstract			
<p>A system and method are provided for controlling the gain of signals transported over transmission media in a distributed antenna network. The system includes a plurality of remote antenna units where each remote antenna unit includes a signal level comparator for comparing the level of the signals received by the respective antenna unit with a predetermined reference level, and a gain controller for reducing the gain of the remote antenna unit when the signal level comparator determines that the level of the received signal exceeds said predetermined reference level. The system may accommodate TDMA systems when a gain controller is used that is fast enough to respond within the time slots of the received signals. As a result, the signal levels output from the remote antenna units are prevented from exceeding a maximum signal level.</p>			

### BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be more fully understood by reading this description in conjunction with the accompanying drawings, which are given by way of illustration only, and thus are not limitative of the present invention, wherein:

- 5      Figure 1 illustrates a conventional distributed antenna network;
- Figure 2 illustrates a block diagram of a known CATV infrastructure which supports PCS;
- 10     Figures 3a and 3b are block diagrams showing embodiments of the system for controlling the gain in a distributed antenna network according to the present invention; and
- 15     Figure 4 is a block diagram showing a system infrastructure in which a gain control system according to an embodiment of the present invention is implemented.

### DETAILED DESCRIPTION

- 15     This invention is directed to a system and method which controls the gain in a plurality of remote antenna units of a distributed antenna network. Often in distributed antenna networks, signals covering a wide range of amplitudes are attempted to be transported from the remote antenna units across transmission media which are not capable of handling such a wide range of amplitudes. In
- 20     particular, when a strong signal is received by a remote antenna unit (for instance, when a mobile station is directly under a remote antenna unit), it is desirable to reduce the gain of the remote antenna unit so that the strong signal does not saturate the system. Accordingly, the system and method of the present invention reduce the level of signals produced by remote antenna units when the received signal level exceeds a predetermined reference level.
- 25

Figure 3(a) illustrates one example of a system that is used in each remote antenna unit for controlling the gain in a distributed antenna network according to the present invention. In this system, a plurality of remote antenna units 100<sub>n</sub> are connected to a network output 150 but only a representative remote antenna

unit 100<sub>n</sub> is shown in Figure 3(a). An antenna 102<sub>n</sub> provides an RF input signal 125<sub>n</sub> to transceiver circuitry 110<sub>n</sub> in this system. The transceiver circuitry 110<sub>n</sub> is connected to a signal strength comparator 130<sub>n</sub> which compares a predetermined reference level with a signal 127<sub>n</sub> which is output from the transceiver circuitry 110<sub>n</sub> and corresponds to the signal 125<sub>n</sub> received by the antenna 102<sub>n</sub>. The signal strength processor 130<sub>n</sub> outputs an error signal 141<sub>n</sub> when the signal 127<sub>n</sub> is determined to exceed the predetermined reference level. A gain controller 142<sub>n</sub> receives the error signal 141<sub>n</sub> and a signal 128<sub>n</sub> which is output from the transceiver circuitry 110<sub>n</sub> and corresponds to the signal 125<sub>n</sub> received by the antenna 102<sub>n</sub>. The gain controller 142<sub>n</sub> controls the gain of the remote antenna unit 100<sub>n</sub> based on the error signal 141<sub>n</sub> such that the output signal 129<sub>n</sub> has the proper level.

The signal strength comparator 130<sub>n</sub> determines whether the level of the signal 129<sub>n</sub> exceeds the maximum level permitted by the transmission media. Therefore, the predetermined reference level is selected to correspond to the maximum permitted level. Typically, this predetermined reference level is set in each remote antenna unit 100<sub>n</sub> upon installation to correspond with maximum signal level capable of being transmitted over the transmission media. However, this predetermined reference level may be later modified after installation for use with different transmission media. The signal strength comparator 130<sub>n</sub> determines whether the signal 127<sub>n</sub> indicates that the signal 129<sub>n</sub> exceeds the predetermined reference level; if so, the error signal 141<sub>n</sub> is generated in proportion to the difference between the signal 127<sub>n</sub> and the predetermined reference level. The error signal 141<sub>n</sub> is input to the gain controller 142<sub>n</sub> and the level of signal 128<sub>n</sub> is reduced in proportion to the level of the error signal 141<sub>n</sub>. As a result, the signal 129<sub>n</sub> output to the network across the transmission media is prevented from saturating the system.

Figure 3(b) illustrates in more detail components that may be preferably used in the block diagram of Figure 3(a). The transceiver circuitry 110<sub>n</sub> includes a low noise amplifier 112<sub>n</sub> connected to the antenna 102<sub>n</sub> for amplifying RF

signals 125<sub>n</sub> generated by the antenna 102<sub>n</sub>. The amplified signal is further processed by a preselecting filter 114<sub>n</sub>, a mixer 116<sub>n</sub> connected to a first local oscillator 118<sub>n</sub>, and an intermediate frequency filter 120<sub>n</sub>. A standard coupler 122<sub>n</sub> sends a portion of this processed signal 127<sub>n</sub> to the signal strength 5 comparator 130<sub>n</sub> and a portion to a mixer 124<sub>n</sub>. The mixer 124<sub>n</sub> is connected to a second local oscillator 126<sub>n</sub> and outputs signal 128<sub>n</sub> to the gain controller 142<sub>n</sub>.

The signal strength comparator 130<sub>n</sub> includes a diode 132<sub>n</sub> for converting the signal 127<sub>n</sub> to a DC level for comparison with the predetermined reference level. A reference level generator 134<sub>n</sub> is used for generating the predetermined 10 reference level. The reference level generator 134<sub>n</sub> is set based on the transmission media as described above. A differential amplifier 136<sub>n</sub> compares the output of the diode 132<sub>n</sub> with the signal output by the reference level generator 134<sub>n</sub>. The comparison output of the differential amplifier 136<sub>n</sub> is further processed by a half wave rectifier 137<sub>n</sub> and a scale and offset amplifier 15 138<sub>n</sub> for outputting the error signal 141<sub>n</sub>. The half wave rectifier 137<sub>n</sub> and the scale and offset amplifier 138<sub>n</sub> process the signal output from the differential amplifier 136<sub>n</sub> so that the gain controller 142<sub>n</sub> may reduce the signal 129<sub>n</sub> to a level below the maximum permitted output level of the transmission media. It will be appreciated that modifications and other circuitry may be used for 20 generating the error signal 141<sub>n</sub> which controls the gain controller 142<sub>n</sub>.

The gain controller 142<sub>n</sub> includes a variable gain amplifier 144<sub>n</sub>. This variable gain amplifier 144<sub>n</sub> controls the level of the signal 129<sub>n</sub> in response to the error signal 141<sub>n</sub>. By choosing the variable gain amplifier 144<sub>n</sub> to be sufficiently fast, the present system may be used in TDMA applications. For 25 example, an analog device AD603 integrated circuit may be used for the variable gain amplifier 144<sub>n</sub> so that the gain of the amplifier responds to the error signal 141<sub>n</sub> within the time slots of TDMA standards.

Figure 4 illustrates an example of a CATV system infrastructure that may advantageously utilize Applicants' system and method for equalizing delay. The 30 infrastructure includes fiber nodes 200,...,200<sub>n</sub> which are connected to a CATV

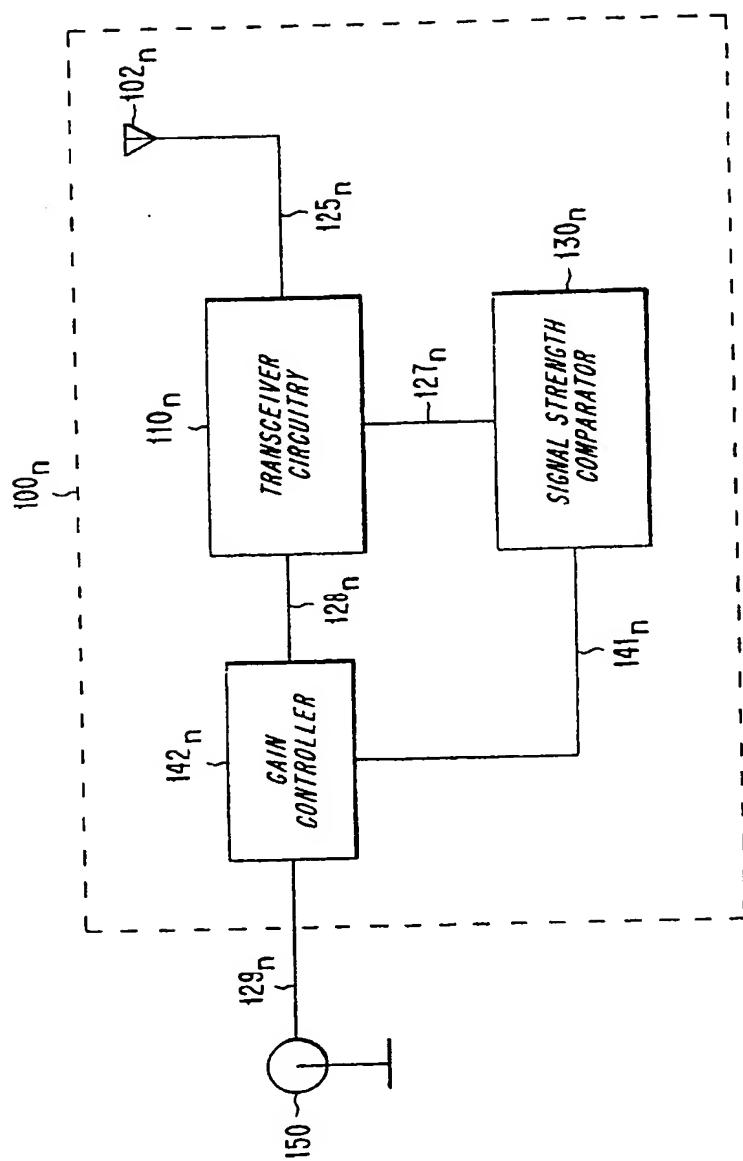


Fig. 3(a)

